

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

nare application of

Docket No: Q89211

Shinichi HORI

Appln. No.: 10/542,576

Group Art Unit: Not Yet Assigned

Confirmation No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: July 19, 2005

For: GA

GAIN-VARIABLE VOLTAGE-CURRENT CONVERSION CIRCUIT AND FILTERING

CIRCUIT INCLUDING THE SAME

LETTER CONCERNING PTO/SB/08 A & B

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants respectfully submit this letter to correct a typographical error on the PTO/SB/08 A & B submitted with the Information Disclosure Statement filed in the above-identified application on July 19, 2005. Applicant listed Japanese Patent Publication No. 50-121643 in the body of the Information Disclosure Statement, however, inadvertently mistyped the reference on the PTO/SB/08 A & B as the corresponding Japanese Utility Model Application No. 49-32100. This reference was cited in the International Search Report of the counterpart PCT Application. For the Examiner's convenience, a courtesy copy of the International Search Report and Japanese Patent Publication No. 50-121643 is being submitted herewith. Therefore, attached is a corrected PTO/SB/08 A & B which accurately lists all of the references submitted on July 19, 2005. The Examiner is respectfully requested to initial this form and return to the applicants for their files.

Respectfully submitted,

SUGHRUE MION, PLLC

Telephone: (202) 293-7060

Facsimile: (202) 293-7860

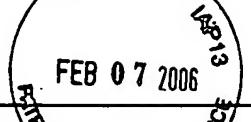
23373
CUSTOMER NUMBER

WASHINGTON OFFICE

Date: February 7, 2006

Howard L. Bernstein

Registration No. 25,665



Substitute for Form 1449 A & B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

| Complete if Known | | | | |
|------------------------|------------------|--|--|--|
| Application Number | 10/542,576 | | | |
| Confirmation Number | Not Yet Assigned | | | |
| Filing Date | July 19, 2005 | | | |
| First Named Inventor | Shinichi HORI | | | |
| Art Unit | Not Yet Assigned | | | |
| Examiner Name | Not Yet Assigned | | | |
| Attorney Docket Number | Q89211 | | | |

| U.S. PATENT DOCUMENTS | | | | | | |
|-----------------------|--------------------------|-----------------|-----------------------------------|------------------|--|--|
| Examiner Initials* | Cite No. ¹ | Document Number | | Publication Date | | |
| | | Number | Kind Code ² (if known) | MM-DD-YYYY | Name of Patentee or Applicant of Cited Documen | |
| | | US | | | | |
| | · | US | | | | |
| | | US | | | | |
| | | US | | | | |
| | <u></u> | US | | | | |
| | | US | | | | |
| | · | US | | | • | |
| | | US | | | | |

| | FOREIGN PATENT DOCUMENTS | | | | | | |
|-----------------------|--------------------------|------------------------------|---------------------|-----------------------------------|------------------|-----------------------------|--------------------------|
| Examiner Initials* | Cite No.1 | Foreign Patent Document | | | Publication Date | Name of Patentee or | Translation ⁶ |
| | | Country Code ³ | Number ⁴ | Kind Code ⁵ (if known) | MM-DD-YYYY | Applicant of Cited Document | Translation |
| | | JР | 1-233813 | Α | 09-19-1989 | | |
| | | JP | 6-342561 | Α | 12-13-1994 | | |
| | - W | JP | 42-14747 | B1 | 08-17-1967 | | |
| | | JP | 50-121643 | Α | 10-04-1975 | | |
| | | JP | 10-242770 | Α | 09-11-1998 | | |
| | | JP | 2000-68761 | Α | 03-03-2000 | | |
| | | JР | 3-64109 | Α | 03-19-1991 | | |
| | | JP | 7-235840 | Α | 09-05-1995 | | |
| | | JР | 2001-36356 | A | 02-09-2001 | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | - | | <u> </u> | | | <u> </u> |
| | | | | | | | |
| | | ļ | | | | | |
| | | | | | | | |

| Examiner Initials* | Cite No.1 | | | | | | |
|--------------------|--------------|---|--|--|--|--|--|
| | · | F. Behbahani, et al. "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE, Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp 476-489. | | | | | |
| | | B. Pankiewicz, et al. "A Field Programmable Analog Array for CMOS Continuous-Time OTA-A Filter Applications", IEEE, Journal of Solid-State Circuits, Vol. 37, No. 2, February 2002, pp 125-136. | | | | | |

| | | |
|--------------------|-----------------|--|
| Examiner Signature | Date Considered | |

[•]EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.